

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,053,739 B2  
APPLICATION NO. : 10/725573  
DATED : May 30, 2006  
INVENTOR(S) : Noboru Wakatsuki et al.

Page 1 of 1

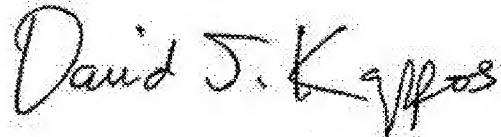
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 22:

Insert claim 15 as follows:

--15. The device according to claim 1, wherein the base and the projections are formed of a silicon material which is at least partially doped with impurities for providing the resistor in the base and the projections.--

Signed and Sealed this  
Twenty-second Day of February, 2011



David J. Kappos  
Director of the United States Patent and Trademark Office